## ABSTRACT OF THE DISCLOSURE

A data processing apparatus comprises a plurality of input signal lines, a plurality of output signal lines and an electronic circuit. The electronic circuit inputs first data from the plurality of input signal lines and outputs second data to the plurality of output signal lines. The first data is one bit data represented by a combination of bits of the plurality of input signal lines. The second data is one bit data represented by a combination of bits of the plurality of output signal lines. The second data is one bit data represented by a combination of bits of the plurality of output signal lines.